

Attorney's Docket No.: 42P17243 Confirmation No.: 9151



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:	
------------------------------	--

Steven M. Bennett, et al.

Examiner: Not yet assigned

Application No.: 10/816,233

Art Unit: 2127

Filed: March 31, 2004

For: Method and Apparatus for Facilitating

Recognition of an Open Event Window During Operation of Guest Software In a Virtual Machine Environment

III a VIItaa Waciiile Environiilen

Mail Stop: Amendment Commissioner for Patents P.O. Box 1450

Alexandria, Virginia 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to MS: Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on December 17, 2004 (Date of Deposit)

Dianne Neathery
(Typed or printed name of person mailing correspondence)

Signature of person mailing correspondence) (Date of signature)

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

X	37 C.F.R. §1.97(b).
	37 C.F.R. §1.97(c). If so, then enclosed with this Information Disclosure Statement is one of the following:
	A statement pursuant to 37 C.F.R. §1.97(e) or
	A check for \$180.00 for the fee under 37 C.F.R. § 1.17(p).
	37 C.F.R. §1.97(d). If so, then enclosed with this Information Disclosure Statement are the following:
	(1) A statement pursuant to 37 C.F.R. §1.97(e); and

(2) A check for \$180.00 for the fee under 37 C.F.R. §1.17(p) for submission of the Information Disclosure Statement.

If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

M. Porhiere

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: December 17, 2004

Marina Portnova Reg. No. 45,750

12400 Wilshire Blvd. Seventh Floor Los Angeles, CA 90025 (408) 720-8300

Substitute for form 1449APTO
INFORMATION DISCLOSURE 10/816,233 **Application Number** STATEMENT BY APPLICAND March 31, 2004 Filing Date Steven M. Bennett First Named Inventor DEC 2 1 2004 2127 **Group Art Unit Examiner Name** Unknown Attorney Docket No: 42P17243 Sheet 1 of 9

		US PA	ATENT DOCUMENTS	
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines,Where Relevant Passages or Relevant Figures Appear
/C.Q/	US- 20010021969	09/13/2001	Burger, Stephen G., et al.	
/C.Q/	US- 20010027511	10/04/2001	Wakabayashi, Masaki, et al.	
/C.Q/	US- 20010027527	10/04/2001	Khidekel, Yuri, et al.	
/C.Q/	US- 20010037450	11/01/2001	Metlitski, Evgueny A., et al.	
/C.Q/	US- 20020007456	01/17/2002	Peinado, Marcus, et al.	
/C.Q/	US- 20020023032	02/21/2002	Pearson, Siani L., et al.	
/C.Q/	US- 20020147916	10/10/2002	Strongin, Geoffrey S., et al.	
/C.Q/	US- 20020166061	11/07/2002	Falik, Ohad, et al.	
/C.Q/	US- 20020169717	11/14/2002	Challener, David C.	
/C.Q/	US- 20030018892	01/23/2003	Tello, Jose	
C.Q/	US- 20030074548	04/17/2003	Cromer, Daryl C., et al.	
/C.Q/	US- 20030115453	06/19/2003	Grawrock, David W.	
C.Q/	US- 20030126442	07/03/2003	Glew, Andrew F., et al.	
/C.Q/	US- 20030126453	07/03/2003	Glew, Andrew F., et al.	
/C.Q/	US- 20030159056	08/21/2003	Cromer, Daryl C., et al.	
'C.Q/	US- 20030188179	10/02/2003	Challener, David C., et al.	
C.Q/	US- 20030196085	10/16/2003	Lampson, Butler W., et al.	
C.Q/	US- 20040117539	06/17/2004	Bennett, Steve, et al.	
C.Q/	US-3,699,532	10/17/1972	Schaffer, Harry G., et al.	
2.0/	US-3,996,449	12/07/1976	Attanasio, Clement R., et al.	
0.0/	US-4,037,214	07/09/1997	Birney, Richard E., et al.	
C.0/	US-4,162,536	07/24/1979	Morley, Richard E.	
/C.Q/	US-4,207,609	06/10/1980	Luiz, Fernando A., et al.	
/C.Q/	US-4,247,905	01/27/1981	Yoshida, Yukihiro, et al.	

EXAMINER

DATE CONSIDERED

PTO: Approved for use through 07/31/2005. Of US Patent & Tredemark Office. U.S. DEPARTMENT O on of information unless it contains a valid OMB or Substitute for form 1449A/PTO
INFORMATION DISCLOSURE Application Number 10/816,233 STATEMENT BY APPLICANT Filing Date March 31, 2004 (Use as many sheets as necessary) First Named Inventor Steven M. Bennett Group Art Unit 2127 **Examiner Name** Unknown Attorney Docket No: 42P17243 Sheet 2 of 9

	US-4,276,594	06/30/1981	Morley, Richard E.	
	US-4,278,837	07/14/1981	Best, Robert M.	
	US-4,307,447	12/22/1981	Provanzano, Salvatore R., et al.	
7C.Q/	US-4,319,233	03/09/1982	Matsuoka, Michihiro, et al.	
/C.0/	US-4,319,323	03/09/1982	Ermolovich, Thomas R., et al.	
7C.Q/	US-4,347,565	08/31/1982	Kaneda, Saburo, et al.	
/C.Q/	US-4,366,537	12/28/1982	Heller, Andrew R., et al.	
/C.Q/	US-4,403,283	09/06/1983	Myntti, Jon N., et al.	
/C.Q/	US-4,419,724	12/06/1983	Branigin, Michael H., et al.	
/C.Q/	US-4,430,709	02/07/1984	Schleupen, Richard, et al.	
/C.Q/	US-4,521,852	06/04/1985	Guttag, Karl M.	
/C.Q/	US-4,571,672	02/18/1986	Hatada, Minoru, et al.	
/C.O/	US-4,621,318	11/04/1986	Maeda, A.	
/C.Q/	US-4,759,064	07/19/1988	Chaum, David L.	
/C.Q/	US-4,795,893	01/03/1989	Ugon, Michael	
7C.Q/	US-4,802,084	01/31/1989	Ikegaya, Hiroshi, et al.	
/C.Q/	US-4,825,052	04/25/1989	Chemin, Francois, et al.	
/C.Q/	US-4,907,270	03/06/1990	Hazard, Michel	
/C.Q/	US-4,907,272	03/06/1990	Hazard, Michel	
/C.Q/	US-4,910,774	03/20/1990	Barakat, Simon	
/C.Q/	US-4,975,836	12/04/1990	Hirosawa, Toshio, et al.	
/C.Q/	US-5,007,082	04/09/1991	Cummins, Mary T.	
/C.Q/	US-5,022,077	06/04/1991	Bealkowski, Richard, et al.	
/C.Q/	US-5,075,842	12/24/1991	Lai, Konrad K.	
/C.Q/	US-5,079,737	01/07/1992	Hackbarth, Holden G.	
/C.Q/	US-5,187,802	02/16/1993	Inoue, Taro, et al.	
/C.Q/	US-5,230,069	02/16/1993	Brelsford, David P., et al.	
/C.Q/	US-5,237,616	08/17/1993	Abraham, Dennis G., et al.	
/C.Q/	US-5,255,379	03/08/1994	Melo, Michael D.	
/C.Q/	US-5,287,363	02/15/1994	Wolf, Paul I., et al.	
76.8/	US-5,293,424	03/08/1994	Hotley, Thomas O., et al.	
/0.0/	US-5,295,251	03/15/1994	Wakui, Fujio, et al.	
/C.Q/	US-5,317,705	05/31/1994	Gannon, Patrick M., et al.	
/C.Q/	US-5,319,760	06/07/1994	Mason, Andrew H., et al.	
/C.Q/	US-5,361,375	11/01/1994	Ogi, Yoshifumi	
/C.Q/	US-5,386,552	01/31/1995	Garney, John L.	
/C.Q/	US-5,421,006	05/30/1995	Jablon, David P., et al.	
/C.O/	US-5,434,999	07/18/1995	Goire, Christian, et al.	
/C.Q/	US-5,437,033	07/25/1995	Inoue, Taro, et al.	
/C.Q/	US-5,442,645	08/15/1995	Ugon, Michel, et al.	
/C.Q/	US-5,455,909	10/03/1995	Blomgren, James S., et al.	
/C.Q/	US-5,459,867	10/17/1995	Adams, Phillip M., et al.	
/C.Q/	US-5,459,869	10/17/1995	Spilo, Michael L.	

/Camquy Truong/ EXAMINER

DATE CONSIDERED

Substitute for form 1449A/PTO	Complete if Known	required to respond to a collection of information unless it contains a valid CMB control nur	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Usa as many sheets as necessary)	Application Number	10/816,233	
	Filing Date	March 31, 2004	
,,	First Named Inventor	Steven M. Bennett	
	Group Art Unit	2127	
	Examiner Name	Unknown	
Sheet 3 of 9	Attorney Docket No: 4	2P17243	

/C.Q/	US-5,469,557	11/21/1995	Salt, Tom, et al.
/C.O/	US-5,473,692	12/05/1995	Davis, Derek L.
7C.Q/	US-5,479,509	12/26/1995	Ugon, Michael
/C.Q/	US-5,504,922	04/02/1996	Seki, Yukihiro, et al.
/C.Q/	US-5,506,975	04/09/1996	Onodera, Osama
/C.Q/	US-5,511,217	04/23/1996	Nakajima, Atsushi, et al.
/C.Q/	US-5,522,075	05/28/1996	Robinson, Paul T., et al.
/C.Q/	US-5,528,231	06/18/1996	Patarin, Jacques
7C.Q/	US-5,533,126	07/02/1996	Hazard, Michel, et al.
7C.Q/	US-5,555,385	09/10/1996	Osisek, Damian L.
/C.Q/	US-5,555,414	09/10/1996	Hough, Roger E., et al.
/C.Q/	US-5,560,013	09/24/1996	Scalzi, Casper A., et al.
/C.Q/	US-5,564,040	10/08/1996	Kubala, Jeffrey P.
/C.Q/	US-5,566,323	10/15/1996	Ugon, Michel
JC_Q/	US-5,568,552	10/22/1996	Davis, Derek L.
/U.Q/	US-5,574,936	11/12/1996	Ryba, Edward G., et al.
/Q.Q/	US-5,582,717	12/10/1996	Di Santo, Dennis E.
7C.Q/	US-5,604,805	02/18/1997	Brands, Stefanus A.
/C.Q/	US-5,606,617	02/25/1997	Brands, Stefanus A.
/C.Q/	US-5,615,263	03/25/1997	Takahashi, Richard J.
/C.Q/	US-5,628,022	05/06/1997	Ueno, Masahiro, et al.
/C.Q/	US-5,633,929	05/27/1997	Kaliski, Jr., Burton S.
/C.O/	US-5,657,445	08/12/1997	Pearce, John J.
/C.Q/	US-5,668,971	11/16/1997	Neufeld, E. D.
/C.Q/	US-5,684,948	11/04/1997	Johnson, James S., et al.
/C.Q/	US-5,706,469	01/06/1998	Kobayashi, Souichi
/C.Q/	US-5,717,903	02/10/1998	Bonola, Thomas J.
/C.Q/	US-5,720,609	02/24/1998	Pfefferle, William C.
/C.Q/	US-5,721,222	02/24/1998	Bernstein, Peter R., et al.
/C Q/	US-5,729,760	03/17/1998	Poisner, David I.
/C.Q/	US-5,737,604	04/07/1998	Miller, David A., et al.
/C.Q/	US-5,737,760	08/07/1998	Grimmer, Jr., George G., et al.
16.8/	US-5,740,178	04/14/1998	Jacks, Steven A., et al.
/C.Q/	US-5,752,046	05/12/1998	Oprescu, Florin, et al.
C.Q/ /C.Q/,	US-5,757,919	05/26/1998	Herbert, Howard C., et al.
/C.Q/,	US-5,764,969	06/09/1998	Kahle, James A.
/C.Q/	US-5,796,835	08/18/1998	Saada, Charles
/C,Q/	US-5,796,845	08/18/1998	Serikawa, Mitsuhiko, et al.
(C.Q/	US-5,805,712	09/08/1998	Davis, Derek L.
/C.Q/	US-5,809,546	09/15/1998	Greenstein, Paul G., et al.
/C.Q/ /C.Q/	US-5,825,875	10/20/1998	Ugon, Michel
/C.Q/	US-5,825,880	10/20/1998	Sudia, Frank W., et al.
/C.Q/	US-5,835,594	11/10/1998	Albrecht, Mark, et al.

/Camquy Truong/ EXAMINER

DATE CONSIDERED

PTO/SH/06-(05-(05) Approved for use through 97/31/2008, OMB 0651-0031

[7C.Q/	US-5,844,986	12/01/1998	Davis, Derek L.
	110 5 050 747	40/00/4000	Bhide, Chandrashekhar W.,
/C.Q/	US-5,852,717	12/22/1998	et al.
/C.Q/	US-5,854,913	12/29/1998	Goetz, John W., et al.
/C.Q/	US-5,867,577	02/02/1999	Patarin, Jacques
/C.Q/	US-5,872,994	02/16/1999	Akiyama, Shin-Ichiro, et al.
7C.Q/	US-5,890,189	03/30/1999	Nozue, Hiroshi, et al.
/C.Q/	US-5,900,606	05/04/1999	Rigal, Vincent
7C.Q/	US-5,901,225	05/04/1999	Ireton, Mark A., et al.
/C.Q/	US-5,903,752	05/11/1999	Dingwall, Thomas J., et al.
7C.Q/	US-5,919,257	07/06/1999	Trostle, Jonathan
/C.Q/	US-5,935,242	08/10/1999	Madany, Peter W., et al.
/C.Q/	US-5,935,247	08/10/1999	Pai, Hsin-Ying, et al.
/C.Q/	US-5,937,063	08/10/1999	Davis, Derek L.
/C.Q/	US-5,944,821	08/31/1999	Angelo, Michael F.
	US-5,953,502	09/14/1999	Helbig, Sr., Walter A.
/C.Q/ /C.Q/	US-5,956,408	09/21/1999	Arnold, Todd W.
	US-5,970,147	10/19/1999	Davis, Derek L., et al.
/C.Q/	US-5,978,475	11/02/1999	Schneier, Bruce, et al.
7C.Q/	US-5,978,481	11/02/1999	Ganesan, Ramanan V., et al.
/C.Q/	US-5,987,557	11/16/1999	Ebrahim, Zahir
/C.Q/	US-6,014,745	01/11/2000	Ashe, Vincent
(C.Q)	US-6,035,374	03/07/2000	Panwar, Ramesh, et al.
(C.Q/	US-6,044,478	03/28/2000	Green, Daniel W.
/C.Q.	US-6,055,637	04/25/2000	Hudson, Jerome D., et al.
/C.Q/	US-6,058,478	05/02/2000	Davis, Derek L.
/C.Q/	US-6,061,794	05/09/2000	Angelo, Michael E.
/C.Q/	US-6,075,938	06/13/2000	Bugnion, Edouard , et al.
/C.Q/	US-6,085,296	07/04/2000	Karkhanis, Nitin Y., et al.
/C.Q/	US-6,088,262	07/11/2000	Nasu, Hiroaki
/C.Q/	US-6,092,095	07/18/2000	Maytal, Benjamin
/C.Q/	US-6,093,213	07/25/2000	Favor, John G., et al.
/C.Q/	US-6,101,584	08/08/2000	Satou, Mitsugu, et al.
/C.Q/_	US-6,108,644	08/22/2000	Goldschlag, David M., et al.
/C.Q/	US-6,115,816	09/05/2000	Davis, Derek L.
/C.Q/	US-6,125,430	09/26/2000	Noel, Karen L., et al.
/C.Q/	US-6,131,166	10/10/2000	Wong-Isley, Becky
/C.Q/	US-6,148,379	11/14/2000	Schimmel, Curt F.
/C.Q/	US-6,158,546	12/12/2000	Hanson, Roger D., et al.
/C.Q/	US-6,173,417	01/09/2001	Merrill, John W.
/C.Q/	US-6,175,924	01/16/2001	Arnold, Todd W.
/C.Q/	US-6,175,925	01/16/2001	Nardone, Joseph M., et al.
/C.Q/	US-6,178,509	01/23/2001	Nardone, Joseph M.
L/C.Q/	US-6,182,089	01/30/2001	Ganapathy, Narayanan, et al.

EYAMINED	/Camquy Truong/

DATE CONSIDERED

Substitute for form 1449A/PTO
INFORMATION DISCLOSURE Application Number 10/816,233 STATEMENT BY APPLICANT Filing Date March 31, 2004 (Use as many sheets as necessary) First Named Inventor Steven M. Bennett **Group Art Unit** 2127 **Examiner Name** Unknown Attorney Docket No: 42P17243 Sheet 5 of 9

$\Gamma/C.O/$	US-6.188.257	02/13/2001	Buer, Mark L.
/C.Q/ /C.Q/	US-6,192,455	02/20/2001	Bogin, Zohar, et al.
	US-6,199,152	03/06/2001	Kelly, Edmund J., et al.
7C.Q/	US-6,205,550	03/20/2001	Nardone, Joseph M., et al.
	US-6,212,635	04/03/2001	Reardon, David C.
/C.Q/	US-6,222,923	04/24/2001	Schwenk, Joerg
/C.Q/	US-6,249,872	06/19/2001	Wildgrube, Frank L., et al.
/Q.Q/	US-6,252,650	06/26/2001	Nakaumra, Kouji
7C.Q/	US-6,269,392	07/31/2001	Cotichini, Christian, et al.
/C.Q/	US-6,272,533	08/07/2001	Browne, Hendrik A., et al.
/C.Q/ /C.Q/	US-6,272,637	08/07/2001	Little, Wendell L., et al.
/C,Q/	US-6,275,933	08/14/2001	Fine, Michael, et al.
/U.U/	US-6,282,650	08/28/2001	Davis, Derek L.
/C.Q/	US-6,282,651	08/28/2001	Ashe, Vincent
/C.Q/	US-6,282,657	08/28/2001	Kaplan, Michael M., et al.
/C.Q/	US-6,292,874	09/18/2001	Barnett, Philip C.
/C.O/	US-6,301,646	10/09/2001	Hostetter, Matthew J.
/C.Q/	US-6,308,270	10/23/2001	Guthery, Scott B., et al.
/C.Q/	US-6,314,409	11/06/2001	Schneck, Paul B., et al.
/C.Q/	US-6,321,314	11/20/2001	Van Dyke, Korbin S.
/U.W/	US-6,327,652	12/04/2001	England, Paul, et al.
/C.Q/	US-6,330,670	12/11/2001	England, Paul, et al.
/C.Q/	US-6,339,815	01/15/2002	Feng, Eugene
/C.O/	US-6,339,816	01/12/2002	Bausch, Jean
10.01	US-6,357,004	03/12/2002	Davis, Derek L.
18:8/	US-6,363,485	03/26/2002	Adams, Carlisle
/C.Q/ /C.Q/	US-6,374,286	04/16/2002	Gee, John K., et al.
/C.Q/.	US-6,374,317	04/16/2002	Ajanovic, Jasmin, et al.
70.Q/	US-6,378,068	04/23/2002	Foster, Mark J.
/C.Q/	US-6,378,072	04/23/2002	Collins, Thomas, et al.
/C.Q/	US-6,389,537	05/14/2002	Davis, Derek L., et al.
/C.Q/	US-6,397,242	05/28/2002	Devine, Scott W., et al.
/C.Q/	US-6,397,379	05/28/2002	Yates, Jr., John S., et al.
/C.O/	US-6,412,035	06/25/2002	Webber, Victor
/C.Q/ /C.Q/	US-6,421,702	07/16/2002	Gulick, Dale E.
7C.Q7	US-6,435,416	08/20/2002	Slassi, Tarik
/C.Q/	US-6,445,797	09/03/2002	McGough, Paul, et al.
7C.Q/	US-6,463,535	10/08/2002	Drews, Paul C., et al.
/C.Q/	US-6,463,537	10/08/2002	Tello, Jose A.
/C.O/	US-6,499,123	12/24/2002	McFarland, Harold L., et al.
1/C.0/	US-6,505,279	01/07/2003	Phillips, Gary, et al.
/C.O/	US-6,507,904	01/14/2003	Ellison, Carl M., et al.
/C.O/	US-6,529,909	03/04/2003	Bowman-Amuah, Michel K.
/C.Q/	US-6,535,988	03/18/2003	Poisner, David L.
L/0.0/	00 0,000,000	100/10/2000	1 Oldier, David E.

/Camquy Truong/ EXAMINER

DATE CONSIDERED

	Under the Paperwork Reduction Act of 1995, no persons are	Approved for use Invogn 07/31/2005. CMII 0551-0031 US Pwert & Tradewark Office U S. DEPARTMENT OF COMMERCE required to respond to a collection of information unless it contains a valid OMS control number
Substitute for form 1449A/PTO	Complete if Known	
INFORMATION DISCLOSURE	Application Number	10/816,233
STATEMENT BY APPLICANT	Filing Date	March 31, 2004
,	First Named Inventor	Steven M. Bennett
	Group Art Unit	2127
	Examiner Name	Unknown
Sheet 6 of 9	Attorney Docket No: 4	I2P17243

/C.Q/	US-6,557,104	04/29/2003	Vu, Son T., et al.
/C.Q/	US-6,560,627	05/06/2003	McDonald, Michael F., et al.
/C.Q/	US-6,609,199	08/19/2003	DeTreville, John
/C.Q/	US-6,615,278	09/02/2003	Curtis, Bryce A.
70.07 70.07	US-6,633,963	10/14/2003	Ellison, Carl M., et al.
7C.Q/	US-6,633,981	10/14/2003	Davis, Derek L.
/C.Q/	US-6,651,171	11/18/2003	England, Paul, et al.
/C,Q/	US-6,678,825	01/13/2004	Ellison, Carl M., et al.
/C.Q/	US-6,684,326	01/27/2004	Cromer, Daryl C., et al.

	Foreign Document No			Pages,Columns,LI nes.Where	
Examiner Initials*		Publication Date	Name of Patentee or Applicant of cited Document	Relevant Passages or Relevant Figures Appear	Τ²
/C.Q/	DE-DE4217444	12/03/1992	Toyohisa, Imada, et al.		
/C.Q/	EP-0602867	06/22/1994	Protheroe, Robert L., et al.		
/C.Q/	EP-EP0473913	03/11/1992	Farrell, Joel A.		
8:8/	EP-EP0600112	06/08/1994	Wimmer, Manfred D.		
U.Q/	EP-EP0892521	01/20/1999	Zamek, Steven		
C.Q/	EP-EP0930567	07/21/1999	Brewer, Jason M.		
Q.Q/	EP-EP0961193	12/01/1999	Laczko, Sr., Frank L.		
/C.Q/ /C.Q/	EP-EP0965902	12/22/1999	Force, Gordon, et al.		
	EP-EP1030237	08/23/2000	Proudler, John, et al.		
/C.Q/	EP-EP1055989	11/29/2000	Proudler, Graeme J., et al.		
/C.Q/	EP-EP1056014	11/29/2000	Proudler, Graeme J., et al.		
/C.Q/	EP-EP1085396	03/21/2001	Proudler, Graeme J., et al.		
/Q.Q/	EP-EP1146715	10/17/2001	Wang, Xin, et al.		
/C.Q/	EP-EP1209563	05/29/2002	England, Paul, et al.		
/C.Q/	EP-EP1271277	01/02/2003	Behar, Yaachov		
/C.Q/	JP-JP2000076139	03/14/2000	Tanno, Masaaki, et al.		
'C O/	WO-WO0021238	04/13/2000	Drews, Paul C.		
0.0/	WO-WO0062232	10/19/2000	Headrick, Samual P., et al.		
10/	WO-WO0127723	04/19/2001	Vamvakas, Athanasia, et al.		
C.Q/	WO-WO0127821	04/19/2001	Chen, Liqun, et al.		
C.Q/	WO-WO0163994	08/30/2001	Van Sant, Glen, et al.		
'C.O/	WO-WO0175564	10/11/2002	Herbert, Howard C., et al.		
2Q/.	WO-WO0175565	10/11/2001	Ellison, Carl M.		
C.Q/	WO-WO0175595	10/11/2001	Ellison, Carl M., et al.		
C.Q/	WO-WO0201794	01/03/2002	Ellison, Carl		
/C O/	WO-WO02060121	08/01/2002	Grawrock, David W.		
/C O/	WO-WO02086684	10/31/2002	Proudler, Graeme J.	1	
70.0/	WO-WO0217555	02/28/2002	Ford, Warwick		_
/C O/	WO-WO03058412	07/17/2003	Glew, Andrew, et al.		

EXAMINER

/Camquy Truong/

DATE CONSIDERED

PTO/SB/084(08-0)

03/04/2009

| Substitute for form 1469A0PTO | INFORMATION DISCLOSURE STATEMENT BY APPLICANT | Use as many pithetic as a recessory) | Sheet 7 of 9 | Attorney Docket No: 42P17243

FOREIGN PATENT DOCUMENTS					
Examiner initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Li nes, Where Relevant Passages or Relevant Figures Appear	T²
/C.Q/	WO-WO9524696	09/14/1995	Mooney, David M., et al.		
7C.Q/	WO-WO9729567	08/14/1997	Gressel, Carmi, et al.		
7C.Q/	WO-WO9812620	03/26/1998	Nishiuchi, Taiki, et al.		
/C.Q/	WO-WO9834365	08/06/1998	Devanbu, Premkumar T., et al.		
/C.Q/	WO-WO9844402	10/08/1998	Bramhill, Ian D., et al.		
7Ç.Q/	WO-WO9905600	02/04/1999	Garst, Blaine, et al.		
/C.Q/	WO-WO9909482	01/15/2002	Bausch, Jean		
/GQ(,	WO-WO9918511	04/15/1999	Edrich, David R.		
/U.Q/	WO-WO9957863	11/11/1999	Hayes Jr., Kent F., et al.		
/C.Q/	WO-WO9965579	12/23/1999	Bond, Eugene T.		

OTHER DOCUMENTS NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No 1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			
/C.Q/		BERG, CLIFF, "How Do I Create a Signed Applet?", <u>Dr. Dobb's Journal</u> , (08/1997),1-9			
/C.Q/		BRANDS, STEFAN, "Restrictive Blinding of Secret-Key Certificates", SPRINGER-VERLAG XP002201306, (1995), Chapter 3			
/C.Q/		CHIEN, ANDREW A., et al., "Safe and Protected Execution for the Morph/AMRM Reconfigurable Processor", 7th Annual IEEE Symposium, FCCM 99 Proceedings, XP010359180, ISBN 0-7695-0375-6, Los Alamitos, CA. (4/21/1999), 209-221			
/C.Q/		COMPAQ COMPUTER CORPORATION, "Trusted Computing Platform Alliance (TCPA) Main Specification Version 1.1a", XP002272822, (1/25/2001), 1-321			
/C.Q/		COULOURIS, GEORGE, et al., "Distributed Systems, Concepts and Designs", 2nd Edition, (1994), 422-424			
/C.Q/		CRAWFORD, JOHN, "Architecture of the Intel 80386", <u>Proceedings of the IEEE International Conference on Computer Design: VLSI in Computers and Processors (ICCD '86)</u> , (October 6, 1986), 155-160			
/C.Q/		DAVIDA, GEORGE I., et al., "Defending Systems Against Viruses through Cryptographic Authentication", <u>Proceedings of the Symposium on Security and Privacy</u> , IEEE Comp. Soc. Press, ISBN 0-8186-1939-2,(May 1989),			
/C.Q/		FABRY, R.S., "Capability-Based Addressing", Fabry, R.S., "Capability-Based Addressing," Communications of the ACM, Vol. 17, No. 7, (July 1974), 403-412			
/C.Q/		FRIEDER, GIDEON "The Architecture And Operational Characteristics of the VMX Host Machine", The Architecture And Operational Characteristics of the VMX Host Machine, IEEE, (1982),9-16			
/C.Q/		GOLDBERG, ROBERT P., "Survey of Virtual Machine Research", COMPUTER Magazine, (06/1974), 34-35			

EXAMINER	/Camquy Truong/	DATE CONSIDERED OOF OF LOOP				
Substitute Discloum Statement Form (PTO-1449)						

00/04/2000

PP.
Approved for use through 07/31/2004
US Pleast & Indexion Citiz US DEPARTMENT
US Pleast & Indexion Citiz US DEPARTMENT
The Propertient Reduction Act of 1995, no persons are required to respond to a collection of information unless if contains a visit OME
UMPLIES IN KNOWN Substitute for form 1449A/PTO
INFORMATION DISCLOSURE **Application Number** 10/816,233 STATEMENT BY APPLICANT
(Use as many sheets as necessary) Filing Date March 31, 2004 First Named Inventor Steven M. Bennett Group Art Unit 2127 **Examiner Name** Unknown Attorney Docket No: 42P17243 Sheet 8 of 9

OTHER ROOM MENTS. NON PATENT LITERATURE ROOM MENTS

OTHER DOCUMENTS NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No 1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²	
		GONG, LI, et al., "Going Beyond the Sandbox: An Overview of the New Security		
/C.Q/		Architecture in the Java Development Kit 1.2", Proceedings of the USENIX		
		Symposium on Internet Technologies and Systems, Monterey, CA,(12/1997),	L	
		GUM, P. H., "System/370 Extended Architecture: Facilities for Virtual		
/C.Q/		Machines", IBM J. Research Development, Vol 27, Number 6, (11/1983), 530-		
		544		
/C.Q/		HEINRICH, JOE, "MIPS R4000 Microprocessor User's Manual, Second		
		Edition", Chapter 4 "Memory Management", (6/11/1993),61-97		
10.07		HP MOBILE SECURITY OVERVIEW, "HP Mobile Security Overview", (Sept.		
/C.Q/		2002), 1-10		
		IBM, "Information Display Technique for a Terminate Stay Resident Program		
/C.Q/		IBM Technical Disclosure Bulletin", TDB-ACC-No. NA9112156, Vol. 34, Issue		
		<u>7A.</u> (12/1/1991),156-158		
		IBM CORPORATION, "IBM ThinkPad T30 Notebooks", IBM Product		
/C.Q/		Specification, located at www-		
		1.ibm.com/services/files/cisco t30 spec sheet 070202.pdf, last visited June 23,		
		2004,(July 2, 2002),1-6		
/C.Q/		INTEL, "IA-32 Intel Architecture Software Developer's Manual", Volume 3:		
		System Programming Guide, Intel Corporation - 2003,13-1 through 13-24		
/c.q/		INTEL, "Intel386 DX Microprocessor 32-Bit CHMOS Microprocessor With		
70.0		Integrated Memory Management", (1995),5-56		
/c.q/		INTEL CORPORATION, "IA-64 System Abstraction Layer Specification", Intel		
70.0		Product Specification, Order Number 245359-001, (01/2000),1-112		
/c.o/ 1		INTEL CORPORATION, "Intel 82802AB/82802AC Firmware Hub (FWH)", Intel		
70.0		Product Datasheet, Document Number 290658-004, (November 2000), 1-6, 17-28		
1 ,00,1		INTEL CORPORATION, "Intel IA-64 Architecture Software Developer's		
/C.Q/		Manual", Volume 2: IA-64 System Architecture, Order Number 245318-001,		
		(01/2000), i, ii, 5.1-5.3, 11.1-11.8, 11.23-11.26		
/C.Q/		KARGER, PAUL A, et al., "A VMM Security Kernal for the VAX Architecture", Proceedings of the Symposium on Research in Security and Privacy.		
/O.G		XP010020182, ISBN 0-8186-2060-9, Boxborough, MA, (5/7/1990),2-19		
		KASHIWAGI, KAZUHIKO, et al., "Design and Implementation of Dynamically		
/C.Q/		Reconstructing System Software", Software Engineering Conference.		
/U.Q/		Proceedings 1996 Asia-Pacific Seoul, South Korea 4-7 Dec. 1996, Los Alamitos.		
		CA USA, IEEE Comput. Soc, US, ISBN 0-8186-7638-8,(1996).		
———		LAWTON, KEVIN, et al., "Running Multiple Operating Systems Concurrently on		
/C.Q/		an IA32 PC Using Virtualization Techniques",		
		http://www.plex86.org/research/paper.txt, (11/29/1999),1-31		
		LUKE, JAHN, et al., "Replacement Strategy for Aging Avionics Computers".		
/C.Q/		IEEE AES Systems Magazine, XP002190614, (March 1999).		
 		MENEZES, ALFRED J., et al., "Handbook of Applied Cryptography", CRC Press		
/C.Q/		LLC, USA XP002201307, (1997), 475		
10.00		LEO, OOM AT 00220 1301, (1991), 413		

EXAMINER	/ouriquy riuong/	DATE CONSIDERED	03/04/2003
	Substitute	Disclosure Statement Form (PTO-1449)	

/Camouy Truopo/

PTO/S8/084(08-03) Approved for use through (7/31/2008, OMB 0851-0031

OTHER ROCHMENTS. MON PATENT LITERATURE ROCHMENTS

Examiler Initials* Cite Include name of the author (in CAPITAL LETTERS), tills of the article (when appropriate), upon the control initials* Cite Color, magazine, beauth, settle and production of the control where published. Cite Color, magazine, beauth, settle and production of the control where published. Cite Cite Color, magazine, beauth, and production of the control where published. Cite Cite Color, and production of the control where published. Cite Cit		OTHER DOCUMENTS NON PATENT LITERATURE DOCUMENTS				
C.Q Series on Discrete Mathematics and its Applications, Boca Raton, FL, XP002165287, ISBN 0849385237, (Oct. 1996), 403-405, 506-515, 570		Cite No 1	publisher, city and/or country where published.	T²		
XP002165287, ISBN 0849385237, (Oct. 1996), 403-405, 506-515, 570						
XP002165287, ISBN 0849385237, (Oct. 1996), 403-405, 506-515, 570 XP002167CAL, "M68040 User's Manual", (1993), 1-1 to 8-32 XRNBA, S., et al., "VM/4: ACOS-4 Virtual Machine Architecture", VM/4: ACOS-4 Virtual Machine Architecture, IEEE, (1985), 171-178 XRCHT, STEFAN, et al., "In-Circult-Emulator Wird Echtzeittauglich", Elektronic, Franzis Verlag GMBH, Munchen, DE, Vol. 40, No. 16, XP000259620,(100-103), 6-1991 XROBIN, JOHN S., et al., "Analysis of the Pentium's Ability to Support a Secure Virtual Machine Monitor", Proceedings of the 9th USENIX Security Symposium, XP002247347, Denver, Colorado, (8/14/00), 1-17 ROSENBLUM, M., "Virtual Platform: A Virtual Machine Monitor for Commodity PC", Proceedings of the 11th Hotchips Conference, (8/17/1999), 185-196 RSA SECURITY, "Hardware Authenticators", www.rsasecurity.com/node.asp?id=1158, 1-2	/C.Q/					
NANBA, S., et al., "VM/4: ACOS-4 Virtual Machine Architecture", VM/4: ACOS-4 Virtual Machine Architecture, IEEE, (1985),171-178 RICHT, STEFAN, et al., "In-Circult-Emulator Wird Echtzeittauglich", Elektronic, Franzis Verlag GMBH, Munchen, DE, Vol. 40, No. 16, XP000259620,(100-103),8-6-1991 ROBIN, JOHN S., et al., "Analysis of the Pentium's Ability to Support a Secure Virtual Machine Monitor", Proceedings of the 9th USENIX Security Symposium, XP002247347, Denver, Colorado, (8/14/00), 1-17 ROSENBLUM, M., "Virtual Platform: A Virtual Machine Monitor for Commodity PC", Proceedings of the 11th Holchips Conference, (8/17/1999), 185-196 RSA SECURITY, "Hardware Authenticators", www.rsasecurity.com/node.asp?id=1158, 1-2 2004 RSA SECURITY, "RSA SecurID Authenticators", www.rsasecurity.com/product/Securid/datashets/SID DS 0103,pdf, 1-2 2004 RSA SECURITY, "Software Authenticators", www.rsasecurity.com/product/Securid/datashets/SID DS 0103,pdf, 1-2 2004 RSA SECURITY, "Software Authenticators", www.rsasecurity.com/product/Securid/datashets/SID DS 0103,pdf, 1-2 2004 RSA SECURITY, "Software Authenticators", www.rsasecurity.com/product/Securid/datashets/SID DS 0103,pdf, 1-2 2004 SAEZ, SERGIO, et al., "A Hardware Scheduler for Complex Real-Time Systems", Proceedings of the IEEE International Symposium on Industrial Electronics, XP002190615,[July 1999),43-48 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithm, and Source Code in C", Wiley, John & Sons, Inc., XP002138607, ISBN 0471117099,(Oct. 1995),66-65 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithms, and Source Code in C", Wiley, John & Sons, Inc., XP0021111449; ISBN 0471117099, (Oct. 1995),66-65 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithms, and Source Code in C", Wiley, John & Sons, Inc., XP00211111449; ISBN 0471117099, (Oct. 1995),66-65 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithms, and Source Code in C", 2nd Edition, Wiley, John & Sons, Inc., XP00211111449; ISBN 0471128457, (Nov. 1995),28-33;			XP002165287, ISBN 0849385237, (Oct. 1996), 403-405, 506-515, 570	l		
A Virtual Machine Architecture, IEEE, (1985),171-178 C.Q/ RICHT, STEFAN, et al., "In-Croul-Emulator Wird Echtzeitlauglich", Elektronic, Franzis Verlag GMBH, Munchen, DE, Vol. 40, No. 16, XP000259620,(100-103),8-6-1991 ROBIN, JOHN S., et al., "Analysis of the Pentium's Ability to Support a Secure Virtual Machine Monitor", Proceedinas of the 9th USENIX Security Symposium, XP000247347, Denver, Colorado, (8/14/00), 1-17 ROSENBLUM, M., "Virtual Platform: A Virtual Machine Monitor for Commodity PC", Proceedinas of the 11th Hotchips Conference, (8/17/1999), 185-196 RSA SECURITY, "Hardware Authenticators", www.rsasecurity.com/norde.asp?id=1158, 1-2 2004 RSA SECURITY, "RSA Securid Authenticators", www.rsasecurity.com/norde.asp?id=1313, 1-2 2004 RSA SECURITY, "Software Authenticators", www.rsasecurity.com/norde.asp?id=1313, 1-2 2004 RSA SECURITY, "Software Authenticators", www.rsasecurity.com/norde.asp?id=1313, 1-2 2004 C.Q/ RSA SECURITY, "Software Authenticators", www.rsasecurity.com/node.asp?id=1313, 1-2 2004 C.Q/ SECURITY, "RSA SECURITY, "Software Authenticators", www.rsasecurity.com/node.asp?id=1313, 1-2 2004 C.Q/ SECURITY, "RSA SECURITY,	/C.Q/		MOTOROLA, "M68040 User's Manual", (1993), 1-1 to 8-32			
A Virtual Machine Architecture, IEEE, (1985),171-178 RICHT, STEFAN, et al., "In-Circul-Emulator Wird Echtzeittauglich", Elektronic, Eranzis Vertag GMBH, Munchen, DE, Vol. 40, No. 16, XP000259620,(100-103),8-6-1991 ROBIN, JOHN S., et al., "Analysis of the Pentium's Ability to Support a Secure Virtual Machine Monitor", Proceedinas of the 9th USENIX Security Symposium, XP002247347, Denver, Colorado, (8/14/00), 1-17 ROSENBLUM, M., "Virtual Platform: A Virtual Machine Monitor for Commodity PC", Proceedinas of the 11th Hotchips Conference, (8/17/1999), 185-196 RSA SECURITY, "Hardware Authenticators", www.rsasecurity.com/node.asp/3fe1158, 1-2 , 2004 RSA SECURITY, "RSA SecurID Authenticators", www.rsasecurity.com/node.asp/3fe1158, 1-2 , 2004 RSA SECURITY, "Software Authenticators", www.rsasecurity.com/node.asp/3fe1158, 1-2 , 2004 RSA SECURITY, "Software Authenticators", www.rsasecurity.com/node.asp/3fe1133, 1-2 , 2004 C.Q/ SAEZ, SERGIO, et al., "A Hardware Scheduler for Complex Real-Time Systems", Proceedings of the IEEE International Symposium on Industrial Electronics, XP02199615, July 1999, 43-48 C.Q/ SOFT, RSA SECURITY, "Software Authenticators", www.rsasecurity.com/node.asp/16=1313, 1-2 , 2004 C.Q/ SAEZ, SERGIO, et al., "A Hardware Scheduler for Complex Real-Time Systems", Proceedings of the IEEE International Symposium on Industrial Electronics, XP02199615, July 1999, 43-48 C.Q/ SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithm, and Source Code in C", Wiley, John & Sons, Inc., XP00239877; ISBN 0471117099, (Oct. 1995), 56-65 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithms, and Source Code in C", Wiley, John & Sons, Inc., XP00211111449; ISBN 0471128457, (Nov. 1995), 28-31, 176-177; 216-217; 461-473; 518-522 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithms, and Source Code in C", 2nd Edition, Wiley, John & Sons, Inc., XP0021111449; ISBN 0471128457, (Nov. 1995), 28-33; 176-177; 216-217; 461-473; 518-522 SHERWOOD, TIMOTHY, et al., "Patchable Instructi	/C O/		NANBA, S., et al., "VM/4: ACOS-4 Virtual Machine Architecture", VM/4: ACOS-			
C.Q Franzis Vertera GMBH, Munchen, DE, Vol. 40, No. 16, XP000259620,(100-103),8-6-1991 ROBIN, JOHN S., et al., "Analysis of the Pentium's Ability to Support a Secure Virtual Machine Monitor, Proceedings of the 9th USENIX Security Symposium, XP002247347, Denver, Colorado, (8/14/00), 1-17 ROSENBLUM, M., "Virtual Platform: A Virtual Machine Monitor for Commodity PC", Proceedings of the 11th Hotchips Conference, (8/17/1999), 185-196 RSA SECURITY, "Hardware Authenticators", waww.rsasecurity.com/node.asp/3fe1158, 1-2 , 2004 RSA SECURITY, "RSA SecuriD Authenticators", waww.rsasecurity.com/node.asp/3fe1158, 1-2 , 2004 RSA SECURITY, "Software Authenticators", waww.rsasecurity.com/node.asp/3fe1131, 1-2 , 2004 RSA SECURITY, "Software Authenticators", waww.srasecurity.com/node.asp/3fe1131, 1-2 , 2004 RSA SECURITY, "Software Authenticators", waww.srasecurity.com/node.asp/3fe1138, 1-2 , 2004 RSA SECURITY, "Software Authenticators", waw.srasecurity.com/node.asp/3fe1	7010					
C.Q. ROBIN, JOHN S., et al., "Analysis of the Pentium's Ability to Support a Secure Inches Vertical Machine Monitor", Proceedings of the 9th USENIX Security Symposium, XP002247347, Denver, Colorado, (8/14/00), 1-17 ROSENBLUM, M., "Virtual Platform: A Virtual Machine Monitor for Commodity PC", Proceedings of the 11th Hotchips Conference, (8/17/1999), 185-196 RSA SECURITY, "Hardware Authenticators", 2004 RSA SECURITY, "Hardware Authenticators", 2004 RSA SECURITY, "RSA SecuriD Authenticators", 2004 RSA SECURITY, "RSA SecuriD Authenticators", 2004 RSA SECURITY, "RSA SecuriD Authenticators", 2004 RSA SECURITY, "Software Authenticators", 2004 RSA SECURITY, "Software Authenticators", 2004	10.01		RICHT, STEFAN, et al., "In-Circuit-Emulator Wird Echtzeittauglich", Elektronic,			
ROBIN, JOHN S., et al., "Analysis of the Pentium's Ability to Support a Secure Virtual Machine Monitor", Proceedings of the 9th USENIX Security Symposium, XP002247347, Denver, Colorado, (8/14/00), 1-17 ROSENBLUM, M., "Virtual Platform: A Virtual Machine Monitor for Commodity PC", Proceedings of the 11th Hotchips Conference, (8/17/1999), 185-196 RSA SECURITY, "Hardware Authenticators", www.rsasecurity.com/node.asp?id=1158, 1-2 , 2004 RSA SECURITY, "RSA SecurID Authenticators", www.rsasecurity.com/node.asp?id=138, 1-2 , 2004 RSA SECURITY, "Software Authenticators", www.rsasecurity.com/node.asp?id=1313, 1-2 , 2004 RSA SECURITY, "Software Authenticators", www.rsasecurity.com/node.asp?id=1313, 1-2 , 2004 RSA SECURITY, "Software Authenticators", www.rsasecurity.com/node.asp?id=1313, 1-2 , 2004 SAEZ, SERGIO, et al., "A Hardware Scheduler for Complex Real-Time Systems", Proceedings of the IEEE International Symposium on Industrial Electronics, XP002199615, July 1999, 43-48 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithm, and Source Code in C", Wiley, John & Sons, Inc., XP002398871; ISBN 0471117099, (Oct. 1995), 56-65 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithm, and Source Code in C", Wiley, John & Sons, Inc., XP002138607; ISBN 0471117099, (Oct. 1995), 56-65 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithms, and Source Code in C", Wiley, John & Sons, Inc., XP0021111449; ISBN 0471117099, (Oct. 1995), 68-65 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithms, and Source Code in C", Wiley, John & Sons, Inc., XP0021111449; ISBN 0471117099, (Oct. 1995), 169-187 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithms, and Source Code in C", 2nd Edition; Wiley, John & Sons, Inc., XP00221117449; ISBN 0471128457, (Nov. 1995), 28-33; 176-177; 216-277; 614-473; 518-522 SHERWOOD, TIMOTHY, et al., "Patchable Instruction ROM Architecture", Department of Computer Science and Engineering, University of California, San	10.00		Franzis Verlag GMBH, Munchen, DE, Vol. 40, No. 16, XP000259620,(100-			
Virtual Machine Monitor, Proceedinas of the 9th USENIX Security Symposium, XP002247347, Denver, Colorado, (8/14/00), 1-17 ROSENBLUM, M., "Virtual Platform: A Virtual Machine Monitor for Commodity PC", Proceedinas of the 11th Hotchips Conference, (8/17/1999), 185-196 RSA SECURITY, "Hardware Authenticators", Waww.rsasecurity.com/product/securid/datasheets/SID_DS_0103.pdf, 1-2 , 20.04 RSA SECURITY, "RSA SecurID Authenticators", Waww.rsasecurity.com/product/securid/datasheets/SID_DS_0103.pdf, 1-2 , 20.04 RSA SECURITY, "Software Authenticators", Waww.rsasecurity.com/product/securid/datasheets/SID_DS_0103.pdf, 1-2 , 20.04 SAEZ, SERGIO, et al., "A Hardware Scheduler for Complex Real-Time Systems", Proceedings of the IEEE International Symposium on Industrial Electronics, XP002190615,(July 1999),43-48 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithm, and Source Code in C", Willey, John & Sons, Inc., XP00239871; ISBN 0471117099,(Oct. 1995),47-52 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithms, and Source Code in C", Willey, John & Sons, Inc., XP0021114449; ISBN 0471117099,(Oct. 1995),169-187 C.Q/						
XP00/2247347, Denver, Colorado, (8/14/00), 1-17						
ROSENBLUM, M., "Virtual Platform: A Virtual Machine Monitor for Commodity PC", Proceedings of the 11th Hotchips Conference, (8/17/1999),185-196 RSA SECURITY, "Hardware Authenticators", 2004 RSA SECURITY, "RSA Security Commode, asp?id=1158, 1-2 , 2004 RSA SECURITY, "RSA Security Duthenticators", www.rsasecurity.com/node.asp?id=1318, 1-2 , 2004 RSA SECURITY, "SSA Security Duthenticators", www.rsasecurity.com/node.asp?id=1313, 1-2 , 2004 RSA SECURITY, "Software Authenticators", www.rsasecurity.com/node.asp?id=1313, 1-2 , 2004 RSA SECURITY, "Software Authenticators", www.rsasecurity.com/node.asp?id=1313, 1-2 , 2004 RSA SECURITY, "Software Authenticators", www.srasecurity.com/node.asp?id=1313, 1-2 , 2004 RSA SECURITY, "Software Authenticators", www.srasecurity.com/node.asp?id=131, 1-2 , 2004 RSA SECURITY, "Software Authenticators", www.srasecurity.com/node.asp?id=131, 1-2 , 2004 RSA SECURITY, "Software Authenticators", www.srasecurity.com/node.asp?id=131, 1-2 ,	/C.Q/		Virtual Machine Monitor", Proceedings of the 9th USENIX Security Symposium,			
C.Q PC", Proceedings of the 11th Hotchips Conference, (8/17/1999), 185-196 RSA SECURITY, "Hardware Authenticators", 2004 RSA SECURITY, "RSA Securif Duthenticators", 2004 RSA SECURITY, "RSA Securif Duthenticators", 2004 RSA SECURITY, "RSA Securif Duthenticators", 2004 RSA SECURITY, "Software Authenticators", 2004 SAEZ, SERGIO, et al., "A Hardware Scheduler for Complex Real-Time 2004 Systems", Proceedings of the IEEE International Symposium on Industrial Electronics, XP002190615, [July 1999), 43-48 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithm, and Source Code in C", Wiley, John & Sons, Inc., XP002939871; ISBN 0471117099, (Oct. 1995), 47-52 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithm, and Source Code in C", Wiley, John & Sons, Inc., XP0021111449; ISBN 0471117099, (Oct. 1995), 48-91-87 C.Q SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithms, and Source Code in C", Wiley, John & Sons, Inc., XP0021111449; ISBN 047117099, (Oct. 1995), 48-91-87 C.Q SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithms, and Source Code in C", 2014 Edition; Wiley, John & Sons, Inc., XP0021111449; ISBN 0471128457, (Nov. 1995), 28-33; 176-177; 216-217; 461-473, 518-522 SHERWOOD, TIMOTHY, et al., "Patchable Instruction ROM Architecture", Department of Computer Science and Engineering, University of California, San			XP002247347, Denver, Colorado, (8/14/00), 1-17			
RSA SECURITY, "Hardware Authenticators",			ROSENBLUM, M., "Virtual Platform: A Virtual Machine Monitor for Commodity			
	/C.Q/					
RSA SECURITY, "RSA SecuriD Authenticators",			RSA SECURITY, "Hardware Authenticators",			
C.Q/	/C.Q/					
C.Q/ RSA SECURITY, "Software Authenticators", 2004 SAEZ, SERGIO, et al., "A Hardware Scheduler for Complex Real-Time Systems", Proceedings of the IEEE International Symposium on Industrial Electronics, XPO2199615,[July 1999),43-48 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithm, and Source Code in C", Wiley, John & Sons, Inc., XP002939871; ISBN 0471117099,(Oct. 1995),47-52 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithm, and Source Code in C", Wiley, John & Sons, Inc., XP002139807; ISBN 0471117099,(Oct. 1995),56-65 C.Q/ SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithms, and Source Code in C", Wiley, John & Sons, Inc., XP0021111449; ISBN 0471117099,(Oct. 1995),69-187 C.Q/ SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithms, and Source Code in C", 2nd Edition; Wiley, John & Sons, Inc., XP00251738; ISBN 0471128457,(Nov. 1995),26-33; 176-177; 216-217; 461-473; 518-522 SHERWOOD, TIMOTHY, et al., "Patchable Instruction ROM Architecture", Department of Computer Science and Engineering, University of California, San	10.01					
C.Q	/C.Q/		www.rsasecurity.com/products/securid/datasheets/SID_DS_0103.pdf, 1-2 / 201	μ 4		
SAEZ, SERGIO , et al., "A Hardware Scheduer for Complex Real-Time Systems", Proceedings of the IEEE International Symposium on Industrial Electronics, XPO2190615,[July 1999),43-48 SCHNEIER, BRUCE , "Applied Cryptography: Protocols, Algorithm, and Source Code in C", Wiley, John & Sons, Inc., XPO02939871; ISBN 0471117099,(Oct. 1995),47-52 SCHNEIER, BRUCE , "Applied Cryptography: Protocols, Algorithm, and Source Code in C", Wiley, John & Sons, Inc., XPO02138607; ISBN 0471117099,(Oct. 1995),56-65 SCHNEIER, BRUCE , "Applied Cryptography: Protocols, Algorithms, and Source Code C", Wiley, John & Sons, Inc., XP0021111449; ISBN 0471117099,(Oct. 1995),69-187 C.Q. SCHNEIER, BRUCE , "Applied Cryptography: Protocols, Algorithms, and Source Code in C", 2nd Edition; Wiley, John & Sons, Inc., XP002251738; ISBN 0471128457,(Nov. 1995),28-31, 176-177; 216-217; 461-473, 518-522 SHERWOOD, TIMOTHY, et al., "Patchable Instruction ROM Architecture", Department of Computer Science and Engineering, University of California, San	10.01					
C.Q Systems*, Proceedings of the IEEE International Symposium on Industrial Electronics, XPO02190615, (July 1999),43-48 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithm, and Source Code in C*, Wiley, John & Sons, Inc., XP002998971; ISBN 0471117099,(Oct. 1995),47-52 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithm, and Source Code in C*, Wiley, John & Sons, Inc., XP002138607; ISBN 0471117099,(Oct. 1995),56-65 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithms, and Source Code C*, Wiley, John & Sons, Inc., XP0021111449; ISBN 047117099,(Oct. 1995),169-187 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithms, and Source Code in C*, 2nd Edition; Wiley, John & Sons, Inc., XP002251738; ISBN 0471128457,(Nov. 1995),28-33; 176-177; 216-217; 461-473; 518-522 SHERWOOD, TIMOTHY, et al., "Patchable Instruction ROM Architecture", Department of Computer Science and Engineering, University of California, San	/U.Q/		www.sraseculity.com/node.asp?id=1515, 1-2			
Electronics, XP002190615,(July 1999),43-48 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithm, and Source Code in C", Wiley, John & Sons, Inc., XP002939871; ISBN 0471117099,(Oct. 1995),47-52 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithm, and Source Code in C", Wiley, John & Sons, Inc., XP002138607; ISBN 0471117099,(Oct. 1995),56-65 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithms, and Source Code C", Wiley, John & Sons, Inc., XP0021111449; ISBN 047117099,(Oct. 1995),169-187 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithms, and Source Code oct." Applied Cryptography: Protocols, Algorithms, and Source Code in C", 2nd Edition; Wiley, John & Sons, Inc., XP00251738; ISBN 0471128457,(Nov. 1995),28-33; 176-177; 216-217, 461-473, 518-522 SHERWOOD, TIMOTHY, et al., "Patchable Instruction ROM Architecture", Department of Computer Science and Engineering, University of California, San	l					
SCHNEIER, BRUCE , "Applied Cryptography: Protocols, Algorithm, and Source Code in C", Wiley, John & Sons, Inc., XP002939871; ISBN 0471117099,(Oct. 1995),47-52	/C.Q/					
C.Q Code in C", Wiley, John & Sons, Inc., XP002939871; ISBN 0471117099,(Oct. 1995).47-52 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithm, and Source Code in C", Wiley, John & Sons, Inc., XP002138607; ISBN 0471117099,(Oct. 1995).66-65 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithms, and Source Code C", Wiley, John & Sons, Inc., XP00211111449; ISBN 047117099,(Oct. 1995),169-187 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithms, and SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithms, and Source Code in C", 2nd Edition; Wiley, John & Sons, Inc., XP002251738; ISBN 0471128457,(Nov. 1995).28-33; 176-177; 216-217; 461-473; 518-522 SHERWOOD, TIMOTHY, et al., "Patchable Instruction ROM Architecture", Department of Computer Science and Engineering, University of California, San						
1995),47-52						
1995),47-52	/c.o/					
C.Q/	70.00					
1995),56-65 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithms, and Source Code C", Wiley, John & Sons, Inc., XP0021111449; ISBN 0471117099,(Oct. 1995),169-187 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithms, and Source Code in C", 2nd Edition; Wiley, John & Sons, Inc., XP002251738; ISBN 0471128457,(Nov. 1995),28-33; 176-177; 216-217; 461-473; 518-522 SHERWOOD, TIMOTHY, et al., "Patchable Instruction ROM Architecture", Department of Computer Science and Engineering, University of California, San						
1995),56-65 SCHNEIER, BRUCE , "Applied Cryptography: Protocols, Algorithms, and Source Code C", Wiley, John & Sons, Inc., XP0021111449; ISBN 0471117098) (Cot. 1995), 169-187 C.O. SCHNEIER, BRUCE , "Applied Cryptography: Protocols, Algorithms, and Source Code in C", 2nd Edition; Wiley, John & Sons, Inc., XP002251738; ISBN 0471128457, (Nov. 1995),28-33; 176-177; 216-217; 461-475; 518-522 SHERWOOD, TIMOTHY , et al., "Patchable Instruction ROM Architecture", Department of Computer Science and Engineering, University of California, San	/c.a/					
C.Q. Source Code C*, Wiley, John & Sons, Inc., XF0021111449; ISBN 0471117099, [Oct. 1995], 169-1817 C.Q. SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithms, and Source Code in C*, 2nd Edition; Wiley, John & Sons, Inc., XF002251738; ISBN 0471128457, [Nov. 1995], 28-33; 176-177; 216-217; 461-473; 518-522 SHERWOOD, TIMOTHY, et al., "Patchable Instruction ROM Architecture", Department of Computer Science and Engineering, University of California, San	.0.0.					
0471117099,(Oct. 1995),169-187 SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithms, and Source Code in C", 2nd Edition; Wiley, John & Sons, Inc., XP002251738; ISBN 0471128457,(Nov. 1995),28-33; 176-177; 216-217; 461-473; 518-522 SHERWOOD, TIMOTHY, et al., "Patchable Instruction ROM Architecture", Department of Computer Science and Engineering, University of California, San						
/C.Q/ SCHNEIER, BRUCE , "Applied Cryptography: Protocols, Algorithms, and Source Code in C", 2nd Edition; Wiley, John & Sons, Inc., XP002251738; ISBN 0471128457,(Nov. 1995);28-33; 176-177; 216-217; 461-473; 518-522 SHERWOOD, TIMOTHY , et al., "Patchable Instruction ROM Architecture", Department of Computer Science and Engineering, University of California, San	/C.Q/					
Source Code in C*, 2nd Edition; Wiley, John & Sons, Inc., XP002251738; ISBN 0471128457 (Nov. 1995) 28-33; 176-177; 216-217; 461-473; 518-522 SHERWOOD, TIMOTHY , et al., "Patchable Instruction ROM Architecture", Department of Computer Science and Engineering, University of California, San						
0471128457,(Nov. 1995);28-33; 176-177; 216-217; 461-473; 518-522 SHERWOOD, TIMOTHY, et al., "Patchable Instruction ROM Architecture", [C. (.) Department of Computer Science and Engineering, University of California, San	1 1001					
SHERWOOD, TIMOTHY, et al., "Patchable Instruction ROM Architecture", Department of Computer Science and Engineering, University of California, San	/U.Q/					
/C O/ Department of Computer Science and Engineering, University of California, San						
Diego, La Jolla, CA, (Nov. 2001).	l /c.o/					
			Diego, La Jolla, CA, (Nov. 2001).			

EXAMINER	/Camquy Truong/	DATE CONSIDERED	03/04/2009		
Substitute Discharge Statement Form (STD) 14460					